

Product Specification

Intel® Solid-State Drive DC S3610 Series

Power Management

- 2.5 inch: 5V or 5V+12V SATA Supply Rail¹¹
- 1.8 inch: 3.3V SATA Supply Rail
- SATA Interface Power Management
- OS-aware hot plug/removal
- Enhanced power-loss data protection feature

- Active: Up to 6.8 W (TYP)⁸
- Idle: 600 mW

Weight:

- 2.5-inch 100GB, 200GB, 400GB & 480GB: 82 grams ± 2
- 2.5-inch 800GB: 88 grams ± 2 grams
- 2.5-inch 1.2-1.6TB: 94 grams ± 2 grams
- 1.8-inch 200GB: 88 grams ± 2 grams
- 1.8-inch 400-800GB: 94 grams ± 2 grams

Temperature

- Operating: 0° C to 70° C
- Non-Operating¹³: -55° C to 95° C
- Temperature monitoring and logging
- Thermal throttling

Shock (operating and non-operating): 1,000 G/0.5 ms

Vibration

- Operating: 2.17 G_{RMS} (5-700 Hz)
- Non-Operating: 3.13 GRMS (5-800 Hz)

Reliability

- Uncorrectable Bit Error Rate (UBER):
 - 1 sector per 10^{^17} bits read
- Mean Time Between Failures (MTBF): 2 million hours
- End-to-End data protection

Certifications and Declarations

- UL*, CE*, C-Tick*, BSMI*, KCC*, Microsoft* WHCK, VCCI*, SATA-IO*

Compatibility

- Windows 7* and Windows 8*, and Windows 8.1*
- Windows Server 2012* R2
- Windows Server 2012*
- Windows Server 2008* Enterprise 32/64bit SP2
- Windows Server 2008* R2 SP1
- Windows Server 2003* Enterprise R2 64bit SP2
- VMWare* 5.1, 5.5
- Red Hat* Enterprise Linux* 5.5, 5.6, 6.1, 6.3, 7.0
- SUSE* Linux* Enterprise Server 10, 11 SP1
- CentOS* 64bit 5.7, 6.3
- Intel® SSD Toolbox with Intel® SSD Optimizer

Capacity:

- 2.5-inch: 100GB, 200GB, 400GB, 480GB, 800GB, 1.2TB,
- 1.8-inch: 200GB, 400GB, 800GB

Components:

- Intel® 20nm NAND Flash Memory
- High Endurance Technology (HET) Multi-Level Cell (MLC)
- Form Factor: 2.5-inch and 1.8-inch
- Read and Write IOPS^{1,2}

(Full LBA Range, IOMeter* Queue Depth 32)

- Random 4KB3 Reads: Up to 84,000 IOPS
- Random 4KB Writes: Up to 28,000 IOPS
- Random 8KB³ Reads: Up to 52,000 IOPS
- Random 8KB Writes: Up to 14,000 IOPS

Bandwidth Performance¹

- Sustained Sequential Read: Up to 550 MB/s4
- Sustained Sequential Write: Up to 520 MB/s
- Endurance: 3 drive writes per day⁵ for 5 years
 - 100GB: 0.5PB - 200GB: 1.1PB
 - 400GB: 3.0PB - 480GB: 3.7PB - 800GB 2.5": 5.3PB - 800GB 1.8": 4.2PB
 - 1.2TB: 8.6PB - 1.6TB: 10.7PB
- Latency (average sequential)
 - Read: 55 μs (TYP)
 - Write: 66 μs (TYP)
- Quality of Service^{6,8}
 - Read/Write: 500 μs / 5 ms (99.9%)
- Performance Consistency^{7,8}
 - Read/Write: Up to 90%/90% (99.9%)
- AES 256-bit Encryption
- Altitude⁹
 - Operating: -1,000 to 10,000 ft
 - Operating 10: 10,000 to 15,000 ft
 - Non-operating: -1,000 to 40,000 ft
- Product Ecological Compliance
 - RoHS*

Compliance

- SATA Revision 3.0; compatible with SATA 6Gb/s, 3Gb/s and 1.5Gb/s interface rates
- ATA/ATAPI Command Set 2 (ACS-2 Rev 7); includes SCT (Smart Command Transport) and device statistics log support
- Enhanced SMART ATA feature set
- Native Command Queuing (NCQ) command set
- Data set management Trim command
- Performance values vary by capacity
- Performance specifications apply to both compressible and incompressible data 4KB = 4,096 bytes; 8KB = 8,192 bytes.
- MB/s = 1,000,000 bytes/second Based on JESD218 standard.
- Based on Random 4KB QD=1 workload, measured as the time taken for 99.9 percentile of commands to finish the round-trip from host to drive and back to host Based on Random 4KB QD=32 workload, measured as the (IOPS in the 99.9th percentile slowest 1-second interval)/(average IOPS during the test)

 Measurement taken once the workload has reached steady state but including all background activities required for normal operation and data reliability

- Altitude pressure is simulated in a test chamber; excludes soft error Extended operation at a higher altitude might impact reliability.
- If both 12V and 5V power supplies are present, defaults to 5V+ 12V power supplies. Does not support 12 volt only Based on 5V power supply Please contact your Intel representative for details on the non-operating temperature range



Ordering Information

Contact your local Intel sales representative for ordering information.

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Revision History

Revision	Description	Date	
001	Initial release.	January 2015	
002	Added 1.8" SKUs spec	February 2015	
003	Updated values in Table 1, "User-Addressable Sectors."	March 2015	
004	Table 18, SMART Attributes: SMART Attribute EAh - added bytes description Added new SMART Attribute F3h description and status flags Section 5.4.2: Updated SMART Attribute E2h decimal precision points from 3 to 2 Added 100GB capacity and specifications	July 2015	

Terminology

Term	Definition
ATA	Advanced Technology Attachment
DAS	Device Activity Signal
DMA	Direct Memory Access
ECC	Error Correction Code
EXT	Extended
FPDMA	First Party Direct Memory Access
GB	Gigabyte. Note : The total usable capacity of the SSD may be less than the total physical capacity because a small portion of the capacity is used for NAND flash management and maintenance purposes.
Gb	Gigabit
HDD	Hard Disk Drive
HET	High Endurance Technology
KB	Kilobyte
I/O	Input/Output
IOPS	Input/Output Operations Per Second
ISO	International Standards Organization
LBA	Logical Block Address
MB	Megabyte (1,000,000 bytes)
MLC	Multi-level Cell
MTBF	Mean Time Between Failures
NCQ	Native Command Queuing
NOP	No Operation
PB	Petabyte
PCB	Printed Circuit Board
PIO	Programmed Input/Output
RDT	Reliability Demonstration Test
RMS	Root Mean Square
SATA	Serial Advanced Technology Attachment
SCT	SMART Command Transport



Term	Definition
SMART	Self-Monitoring, Analysis and Reporting Technology. This is an open standard for developing hard drives and software systems that automatically monitors the health of a drive and reports potential problems.
SSD	Solid-State Drive
ТВ	Terabyte
TYP	Typical
UBER	Uncorrectable Bit Error Rate

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1 Overview

This document describes the specifications and capabilities of the Intel® SSD DC S3610 Series.

The Intel SSD DC S3610 Series delivers excellent performance and Quality of Service combined with high reliability for Serial Advanced Technology Attachment (SATA)-based computers in capacities of 100GB, 200GB, 400GB, 480GB, 800GB, 1.2TB and 1.6TB.

By combining 20nm Intel® NAND Flash Memory technology with SATA 6Gb/s interface support, the Intel SSD DC S3610 Series delivers Sequential Read speeds of up to 550 MB/s and Sequential Write speeds of up to 520 MB/s. The Intel SSD DC S3610 Series also delivers Random 4k Read speeds of up to 84,000 IOPS and Random 4K Write speeds of up to 28,000 IOPS, and Quality of Service of 500 μ s for random 4KB reads measured at a queue depth of 1.

The industry-standard 2.5-inch and 1.8-inch form factors enable interchangeability with existing hard disk drives (HDDs) and native SATA HDD drop-in replacement with the enhanced performance, reliability, ruggedness, and power savings offered by an SSD.

Intel SSD DC S3610 Series offers these key features:

- Standard Endurance Technology
- High I/O and throughput performance
- Consistent I/O latency
- Enhanced power-loss data protection
- End-to-End data protection
- Thermal throttling
- Temperature Sensor
- Inrush current management
- Low power
- High reliability
- Temperature monitor and logging
- Power loss protection capacitor self-test



2 Product Specifications

2.1 Capacity

Table 1: User Addressable Sectors

Intel SSD DC S3610 Series	Unformatted Capacity (Total User Addressable Sectors in LBA Mode)
100GB	195,371,568
200GB	390,721,968
400GB	781,422,768
480GB	937,703,088
800GB	1,562,824,368
1.2TB	2,344,225,968
1.6GB	3,125,627,568

Notes:

1GB = 1,000,000,000 bytes; 1 sector = 512 bytes.

LBA count shown represents total user storage capacity and will remain the same throughout the life of the drive.

The total usable capacity of the SSD may be less than the total physical capacity because a small portion of the capacity is used for NAND flash management and maintenance purposes.

2.2 Performance

Table 2: Random Read/Write Input/Output Operations Per Second (IOPS)

		Intel SSD DC S3610 Series							
Specification ¹	Unit	100GB (2.5")	200GB (2.5"/1.8")	400GB (2.5"/1.8")	480GB (2.5")	800GB (2.5"/1.8")	1.2TB (2.5")	1.6TB (2.5")	
Random 4KB Read (up to) ²	IOPS	82,000	84,000/ 70,000	84,000/ 70,000	84,000	84,000/ 70,000	84,000	84,000	
Random 4KB Write (up to)	IOPS	6,500	12,000/ 11,000	25,000/ 22,000	28,000	28,000/ 17,000	28,000	27,000	
Random 8KB Read (up to) ³	IOPS	45,000	52,000/ 48,000	52,000/ 47,000	52,000	52,000/ 46,000	52,000	52,000	
Random 8KB Write (up to)	IOPS	3,200	6,000/ 5,500	12,000/ 11,000	14,000	14,000/ 9,000	15,000	14,000	
Random 4KB 70/30 Read/Write (up to) ²	IOPS	17,000	30,000/ 28,000	46,000/ 40,000	49,000	49,000/ 37,000	51,000	50,000	
Random 8KB 70/30 Read/Write (up to) ³	IOPS	9,000	15,000/ 14,000	25,000/ 23,000	28,000	29,000/ 21,000	30,000	29,000	

Notes:

- 1. Performance measured using IOMeter* with Queue Depth 32. Measurements are performed on a full Logical Block Address (LBA) span of the drive.
- 2. 4KB = 4,096 bytes
- 3. 8KB = 8,192 bytes



Table 3: Random Read/Write IOPS Consistency

		Intel SSD DC S3610 Series							
Specification ¹	Unit	100GB (2.5")	200GB (2.5"/1.8")	400GB (2.5"/1.8")	480GB (2.5")	800GB (2.5"/1.8")	1.2TB (2.5")	1.6TB (2.5")	
Random 4KB Read (up to) ²	%	90	90/90	90/90	90	90/90	90	90	
Random 4KB Write (up to)	%	90	90/90	90/90	90	90/85	90	90	
Random 8KB Read (up to) ³	%	75	90/90	90/90	90	90/90	90	90	
Random 8KB Write (up to)	%	90	90/90	90/90	90	90/90	90	90	

Notes:

- 1. Performance consistency measured using IOMeter* based on Random 4KB QD=32 workload, measured as the (IOPS in the 99.9th percentile slowest 1-second interval) / (average IOPS during the test). Measurements are performed on a full Logical Block Address (LBA) span of the drive once the workload has reached steady state but including all background activities required for normal operation and data reliability
- 2. 4KB = 4,096 bytes
- 3. 8KB = 8,192 bytes

Table 4: Sequential Read and Write Bandwidth

		Intel SSD DC S3610 Series							
Specification	Unit	100GB (2.5")	200GB (2.5"/1.8")	400GB (2.5"/1.8")	480GB (2.5")	800GB (2.5"/1.8")	1.2TB (2.5")	1.6TB (2.5")	
Sequential Read (SATA 6Gb/s) 1	MB/s	410	550/500	550/500	550	550/500	550	550	
Sequential Write (SATA 6Gb/s) 1	MB/s	110	230/220	400/370	450	520/380	500	500	

Note:

Table 5: Latency

	Intel SSD DC S3610 Series				
Specification	100GB/200GB/400GB/ 480GB	800GB/1.2TB/ 1.6TB			
Latency ¹ (TYP)					
Read	55 μs	55 μs			
Write	66 μs	66 μs			
Power On to Ready ²	5.0 s	7.0 s			

Notes:

- 1. Device measured using IOMeter*. Latency measured using 4KB (4,096 bytes) transfer size with Queue Depth equal to 1 on a sequential workload.
- 2. Power On To Ready time assumes proper shutdown. Time varies if shutdown is not preceded by STANDBY IMMEDIATE command. For 95% of the time, the maximum time for power on to ready will be less than 15 seconds.

^{1.} Performance measured using IOMeter* with 128KB (131,072 bytes) of transfer size with Queue Depth 32.



Table 6: **Quality of Service 2.5"**

		Intel 2.5" SSD DC S3610 Series						
Specification	Unit	Queue I	Depth=1	Queue Depth=32				
·		100GB/200GB/ 400GB/480GB	800GB/1.2TB/ 1.6TB	100GB/200GB/ 400GB/480GB	800GB/1.2TB/ 1.6TB			
Quality of Service ^{1,2} (99.9%)								
Reads	ms	2	5	5	5			
Writes	ms	5	0.5	15/10/10/10	10/5/5			
Quality of Service ^{1,2} (99.9999%)								
Reads	ms	10	10	10	10			
Writes	ms	15	10	30	20/30/30			

Table 7: Quality of Service 1.8"

		Intel 1.8" SSD DC S3610 Series					
Specification	Unit	Queue l	Depth=1	Queue Depth=32			
		200GB	400GB/800GB	200GB	400GB/800GB		
Quality of Service ^{1,2} (99.9%)							
Reads	ms	0.2	0.2/2	5	5		
Writes	ms	2	0.5/1	10	10		
Quality of Service ^{1,2} (99.9999%)							
Reads	ms	10	5/10	10	10		
Writes	ms	15	10	20	20/30		

- 1. Device measured using IOMeter*. Quality of Service measured using 4KB (4,096 bytes) transfer size on a random workload on a full Logical Block Address (LBA) span of the drive once the workload has reached steady state but including all background activities required for normal operation and data reliability.
- 2. Based on Random 4KB QD=1, 32 workloads, measured as the time taken for 99.9(or 99.9999) percentile of commands to finish the round-trip from host to drive and back to host.

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2.3 Electrical Characteristics

Table 8: Operating Voltage for 2.5-inch Form Factor

EL	Intel SSDDC S3610 Series	
Electrical Characteristics	100GB, 200GB, 400GB, 480GB, 800GB, 1.2TB, 1.6TB	
5V Operating Characteristics: Operating Voltage range Inrush Current (Typical Peak) ¹	5V (±5%) 1.2A for the first 1s	
12V Operating Characteristics: Operating Voltage range Inrush Current (Typical Peak) ¹	12V (±10%) 1.2A for the first 1s	

Note: Measured from initial device power supply application.

Table 9: Power Consumption for 2.5-inch Form Factor (5V Supply)

		Intel SSD DC S3610 Series							
Specification	Unit	100GB	200GB	400GB	480GB	800GB	1.2TB	1.6TB	
Active Write - RMS Average ¹	W	3.3	3.3	4.7	5.3	6.3	6.4	6.8	
Active Write - Burst ²	W	2.0	3.6	5.7	6.6	8.7	10.0	10.7	
Active Write - Max Burst ³	W	4.8	4.9	7.4	9.0	11.3	11.6	12.2	
Active Read - RMS Average ⁴	W	3.3	2.6	2.7	2.7	2.9	3.0	3.3	
Active Read - Burst ⁵	W	2.5	1.4	4.5	4.3	4.5	4.9	5.7	
Active Read – Max Burst ⁶	W	4.5	4.3	5.6	5.3	6.2	5.9	6.5	
Idle	W	0.53	0.54	0.57	0.57	0.61	0.63	0.62	

Notes:

- 1. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential writes. RMS (Root Mean Squared) Average Power is measured using Agilent Power Analyzer over a 100 ms sample period with PLI capacitor charge enabled.
- 2. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential writes. RMS (Root Mean Squared) Burst Power is measured using Agilent Power Analyzer over a 500 μs sample period with PLI capacitor charge disabled.
- 3. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential writes. RMS (Root Mean Squared) Max Burst power is measured using Agilent Power Analyzer over a 500 µs sample period with PLI capacitor charge enabled. Pulse is 0.25% of total time.
- 4. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential reads. RMS (Root Mean Squared) average power is measured using Agilent Power Analyzer over a 100 ms sample period with PLI capacitor charge enabled.
- 5. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential reads. RMS (Root Mean Squared) burst power is measured using Agilent Power Analyzer over a 500 μs sample period with PLI capacitor charge disabled.
- The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential reads. RMS (Root Mean Squared) Max Burst power is
 measured using Agilent Power Analyzer over a 500 μs sample period with PLI capacitor charge enabled. Pulse is approximately 0.25% of
 total time.

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Table 10: Power Consumption for 2.5-inch Form Factor (5V + 12V Supply)

		Intel SSD DC S3610 Series													
Specification ¹	Unit	100	OGB	200	OGB	400	OGB	480	OGB	800	OGB	1.2	ТВ	1.6	ТВ
		5V	12V	5V	12V	5V	12V	5V	12V	5V	12V	5V	12V	5V	12V
Active Write - RMS Average ¹	W	2.6	1.2	1.6	2.0	1.8	3.2	1.9	3.6	2.1	4.5	2.1	4.1	2.3	4.5
Active Write - Burst ²	W	2.0	1.3	2.1	2.2	2.4	4.2	2.5	5.1	2.3	7.1	2.9	7.8	3.0	8.2
Active Write - Max Burst ³	W	4.0	1.3	3.4	2.2	3.9	4.1	4.5	5.0	4.5	7.8	4.7	7.7	4.5	8.2
Active Read - RMS Average ⁴	W	2.8	0.9	1.8	0.9	1.9	0.9	1.9	0.9	2.0	0.9	2.2	1.0	2.3	1.7
Active Read - Burst⁵	W	1.9	1.0	2.0	1.4	2.3	2.0	2.4	2.0	2.4	3.6	2.8	4.1	3.0	1.7
Active Read – Max Burst ⁶	W	3.9	0.9	3.3	1.4	3.6	2.0	4.4	2.1	4.4	2.1	4.3	2.5	4.5	5.4
Idle	W	0.5	0.1	0.54	0.01	0.57	0.01	0.58	0.01	0.61	0.01	0.63	0.01	0.62	0.01

Notes:

- 1. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential writes. RMS (Root Mean Squared) Average Power is measured using Agilent Power Analyzer over a 100 ms sample period with PLI capacitor charge enabled.
- 2. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential writes. RMS (Root Mean Squared) Burst Power is measured using Agilent Power Analyzer over a 500 μs sample period with PLI capacitor charge disabled.
- 3. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential writes. RMS (Root Mean Squared) Max Burst power is measured using Agilent Power Analyzer over a 500 µs sample period with PLI capacitor charge enabled. Pulse is 0.25% of total time.
- 4. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential reads. RMS (Root Mean Squared) average power is measured using Agilent Power Analyzer over a 100 ms sample period with PLI capacitor charge enabled.
- 5. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential reads. RMS (Root Mean Squared) burst power is measured using Agilent Power Analyzer over a 500 μs sample period with PLI capacitor charge disabled.
- The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential reads. RMS (Root Mean Squared) Max Burst power is
 measured using Agilent Power Analyzer over a 500 μs sample period with PLI capacitor charge enabled. Pulse is approximately 0.25% of
 total time.

Table 11: Operating Voltage for 1.8-inch Form Factor

	Intel SSD DC S3610 Series		
Electrical Characteristics	inter 33D DC 33010 Series		
	200GB, 400GB, 800GB		
3.3V Operating Characteristics:			
Operating Voltage range	3.3V (±5%)		
Inrush Current (Typical Peak) 1	1.5A for the first 1s		

Note:

1. Measured from initial device power supply application.

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Table 12: Power Consumption for 1.8-inch Form Factor

		Intel SSD DC S3610 Series			
Specification	Unit	200GB	400GB	800GB	
Active Write - RMS Average ¹ @ 3.3V	W	2.9	4.5	5.2	
Active Write – Burst ² @ 3.3V	W	3.3	5.8	6.1	
Active Write - Max Burst ³ @ 3.3V	W	3.8	6.2	6.7	
Active Read - RMS Average ⁴ @ 3.3V	w	2.2	2.4	2.6	
Active Read - Burst⁵ @ 3.3V	w	2.7	4.0	5.5	
Active Read – Max Burst ⁶ @ 3.3V	W	3.3	4.1	5.6	
Idle @ 3.3V	W	0.53	0.55	0.56	

Notes:

- 1. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential writes. RMS (Root Mean Squared) Average Power is measured using Agilent Power Analyzer over a 100 ms sample period with PLI capacitor charge enabled.
- 2. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential writes. RMS (Root Mean Squared) Burst Power is measured using Agilent Power Analyzer over a 500 µs sample period with PLI capacitor charge disabled.
- 3. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential writes. RMS (Root Mean Squared) Max Burst power is measured using Agilent Power Analyzer over a 500 µs sample period with PLI capacitor charge enabled. Pulse is 0.25% of total time.
- 4. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential reads. RMS (Root Mean Squared) average power is measured using Agilent Power Analyzer over a 100 ms sample period with PLI capacitor charge enabled.
- 5. The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential reads. RMS (Root Mean Squared) burst power is measured using Agilent Power Analyzer over a 500 μs sample period with PLI capacitor charge disabled.
- The workload equates 128 KB (131,072 bytes) Queue Depth equal to 32 sequential reads. RMS (Root Mean Squared) Max Burst power is
 measured using Agilent Power Analyzer over a 500 μs sample period with PLI capacitor charge enabled. Pulse is approximately 0.25% of
 total time.



2.4 Environmental Conditions

Table 13: Temperature, Shock, Vibration

Temperature	Range
Case Temperature	
Operating	0 – 70 °C
Non-operating ¹	-55 – 95 °C
Temperature Gradient ²	
Operating	30 °C/hr (Typical)
Non-operating	30 °C/hr (Typical)
Humidity	
Operating	5 – 95 %
Non-operating	5 – 95 %
Shock and Vibration	Range
Shock ³	
Operating	1,000 G (Max) at 0.5 ms
Non-operating	1,000 G (Max) at 0.5 ms
Vibration ⁴	
Operating	2.17 G _{RMS} (5-700 Hz) Max
Non-operating	3.13 G _{RMS} (5-800 Hz) Max

Notes:

- 1. Contact your Intel representative for details on the non-operating temperature range.
- 2. Temperature gradient measured without condensation.
- 3. Shock specifications assume the SSD is mounted securely with the input vibration applied to the drive-mounting screws. Stimulus may be applied in the X, Y or Z axis. Shock specification is measured using Root Mean Squared (RMS) value.
- 4. Vibration specifications assume the SSD is mounted securely with the input vibration applied to the drive-mounting screws. Stimulus may be applied in the X, Y or Z axis. Vibration specification is measured using RMS value.

2.5 Product Regulatory Compliance

Intel SSD DC S3610 Series meets or exceeds the regulatory or certification requirements listed in the table below.

Table 14: Product Regulatory Compliance Specifications

Title	Description	Region For Which Conformity Declared
TITLE 47-Telecommunications CHAPTER 1— FEDERAL COMMUNMICATIONS COMMISSION PART 15 — RADIO FREQUENCY DEVICES	FCC Part 15B Class B	USA
ICES-003, Issue 4 Interference-Causing Equipment Standard Digital Apparatus	CA/CSA-CEI/IEC CISPR 22:02. This is CISPR 22:1997 with Canadian Modifications	Canada
IEC 55024 Information Technology Equipment — Immunity characteristics— Limits and methods of measurement CISPR24:2010	EN-55024: 1998 and its amendments	European Union
IEC 55022 Information Technology Equipment — Radio disturbance Characteristics— Limits and methods of measurement CISPR24:2008 (Modified)	EN-55022: 2006 and its amendments	European Union
EN-60950-1 2 nd Edition	Information Technology Equipment — Safety — Part 1: General Requirements	USA/Canada
UL/CSA EN-60950-1 2 nd Edition	Information Technology Equipment — Safety — Part 1: General Requirements	USA/Canada

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2.6 Reliability

Intel SSD DC S3610 Series meets or exceeds SSD endurance and data retention requirements as specified in the JESD218 standard. Reliability specifications are listed in the table below:

Table 15: Reliability Specifications

Parameter	Value	
Uncorrectable Bit Error Rate (UBER)		
Uncorrectable bit error rate will not exceed one sector in the specified number of bits read. In the unlikely event of a non-recoverable read error, the SSD will report it as a read failure to the host; the sector in error is considered corrupt and is not returned to the host.	< 1 sector per 10 ¹⁷ bits read	
Mean Time Between Failures (MTBF)		
Mean Time Between Failures is estimated based on Telcordia* methodology and demonstrated through Reliability Demonstration Test (RDT).	2 million hours	
Power On/Off Cycles		
Power On/Off Cycles is defined as power being removed from the SSD, and then restored. Most host systems remove power from the SSD when entering suspend and hibernate as well as on a system shutdown.	24 per day	
Insertion Cycles	50 on SATA cable 500 on backplane	
SATA/power cable insertion/removal cycles.		
Data Retention	3 months power-off retention once SSD	
The time period for retaining data in the NAND at maximum rated endurance.	reaches rated write endurance at 40 °C	
Endurance Rating	100GB 2.5": 0.5 PB 200GB 1.8"/2.5": Up to 1.1 PB	
While running JESD218 standard1 and based on JESD219 workload.	400GB 1.8"/2.5": Up to 3.0 PB 480GB: Up to 3.7 PB 800GB 2.5": Up to 5.3 PB 800GB 1.8": Up to 4.2 PB 1.2TB: Up to 8.6 PB 1.6TB: Up to 10.7 PB while running JESD218 standard1	

Note:

^{1.} Refer to JESD218 standard table 1 for UBER, FFR and other Enterprise SSD endurance verification requirements. Endurance verification acceptance criterion based on establishing <1E-16 at 60 confidence.



2.7 Temperature Sensor

The Intel SSD DC S3610 Series has an internal temperature sensor with an accuracy of +/-2C over a range of -20C to +80C which can be monitored using two SMART attributes: Airflow Temperature (BEh) and Device Internal Temperature (C2h).

For more information on supported SMART attributes, see "SMART Attributes" on page 20.

2.8 Power Loss Capacitor Test

The Intel SSD DC S3610 Series supports testing of the power loss capacitor, which can be monitored using the following SMART attribute: (175, AFh).

2.9 Hot Plug Support

Hot Plug insertion and removal is supported in the presence of a proper connector and appropriate operating system (OS), as described in the SATA 3.0 specification.

This product supports asynchronous signal recovery and issues an unsolicited COMINIT when first mated with a powered connector to guarantee reliable detection by a host system without hardware device detection.

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3 Mechanical Information

Figures 1 and 2 show the physical package information for the Intel® SSD DC S3610 Series in the 2.5- and 1.8-inch form factors. All dimensions are in millimeters.

Figure 1: Intel SSD DC S3610 Series 2.5-inch Dimensions

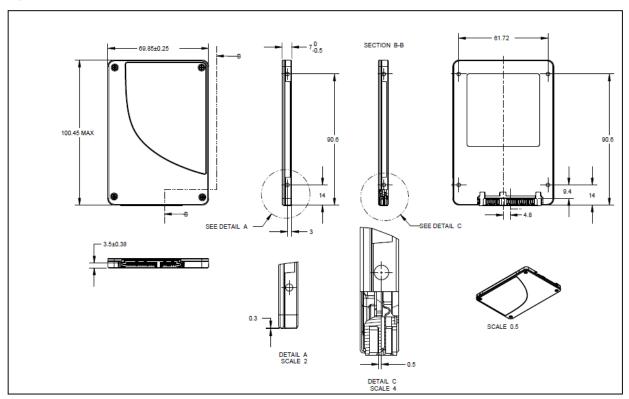
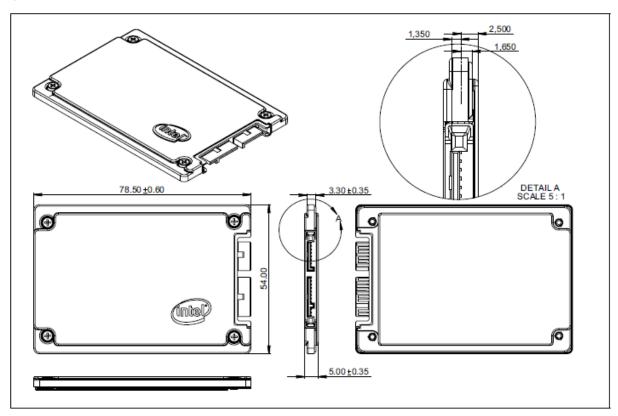




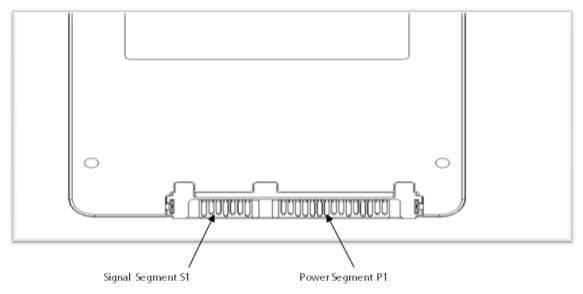
Figure 2: Intel® SSD DC S3610 Series 1.8-inch Dimensions



4 Pin and Signal Descriptions

4.1 2.5-inch Form Factor Pin Locations

Figure 3: Layout of 2.5-inch Form Factor Signal and Power Segment Pins



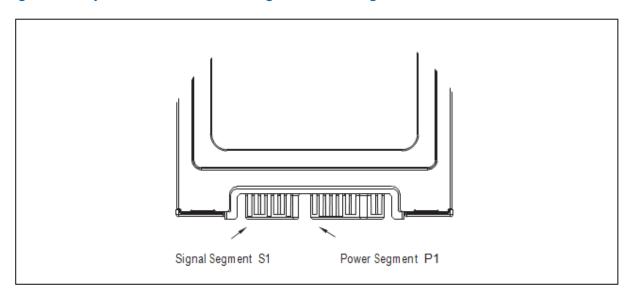
Note: 2.5-inch connector supports built in latching capability.

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4.2 1.8-inch Form Factor Pin Locations

Figure 4: Layout of 1.8-inch Form Factor Signal and Power Segment Pins



4.3 Connector Pin Signal Definitions

Table 16: Serial ATA Connector Pin Signal Definitions—2.5-inch and 1.8-inch Form Factors

Pin	Function	Definition	
S1	Ground	1st mate	
S2	A+	Differential signal pair A	
S3	A-	Differential signal pair A	
S4	Ground	1 st mate	
S5	B-	Differential signal pair D	
S6	B+	Differential signal pair B	
S 7	Ground	1st mate	

Note: Key and spacing separate signal and power segments.

4.4 Power Pin Signal Definitions

Table 17: Serial ATA Power Pin Definitions—2.5-inch Form Factors

Pin ¹	Function	Definition	Mating Order
P1 ²	Not connected	(3.3V Power)	
P2 ²	Not connected	(3.3V Power)	
P3 ²	Not connected	(3.3V Power; pre-charge)	2 nd Mate
P4 ^{3,4}	Ground	Ground	1 st Mate
P5 ³	Ground	Ground	1 st Mate
P6 ³	Ground	Ground	1 st Mate
P7 ^{3,5}	V ₅	5V Power	1 st Mate
P8 ^{3,5}	V ₅	5V Power	2 nd Mate



Pin ¹	Function	Definition	Mating Order
P9 ^{3,5}	V ₅	5V Power	2 nd Mate
P10 ³	Ground	Ground	1 st Mate
P11 ⁶	DAS	Device Activity Signal	2 nd Mate
P12 ^{3,4}	Ground	Ground	1 st Mate
P13 ⁷	V ₁₂	12V Power	1 st Mate
P14 ⁷	V ₁₂	12V Power	2 nd Mate
P15 ⁷	V ₁₂	12V Power	2 nd Mate

Notes:

- 1. All pins are in a single row, with a 1.27 mm (0.050-inch) pitch.
- 2. Pins P1, P2 and P3 are connected together, although they are not connected internally to the device. The host may put 3.3V on these pins.
- 3. The mating sequence is:
 - ground pins P4-P6, P10, P12 and the 5V power pin P7
 - signal pins and the rest of the 5V power pins P8-P9
- 4. Ground connectors P4 and P12 may contact before the other 1st mate pins in both the power and signal connectors to discharge ESD in a suitably configured backplane connector.
- 5. Power pins P7, P8, and P9 are internally connected to one another within the device.
- 6. The host may ground P11 if it is not used for Device Activity Signal (DAS).
- 7. Pins P13, P14 and P15 are internally connected to one another within the device. The host may put 12V on these pins.

Table 18: Serial ATA Power Pin Definitions—1.8-inch Form Factors

Pin	Function	Definition	Mating Order ¹
P1 ²	V ₃₃	3.3V Power	2 nd Mate
P2 ²	V ₃₃	3.3V Power, per-charge	2 nd Mate
P3 ³	Ground		1 st Mate
P4 ³	Ground		1 st Mate
P5 ⁴	V ₅	5V Power; not connected.	1 st Mate
P6⁴	V ₅	5V Power; not connected.	2 nd Mate
P7⁵	DAS	Device Activity Signal	2 nd Mate
Key	Key	NC	NC
P8 ⁶	Optional	Manufacturing Test Pin	2 nd Mate
P9 ⁶	Optional	Manufacturing Test Pin	2 nd Mate

Notes:

- 1. All mate sequences assume zero angular offset between connectors.
- 2. P1 and P2 are internally connected to one another within the device.
- Ground connectors P3 and P4 may contact before the other 1st mate pins in both the power and signal connectors to discharge ESD in a suitably configure backplane connector.
- 4. Pins P5 and P6 are not connected internally to the device but there is an option to connect through a zero ohm stuffing resistor. The host may put 5V on these pins.
- 5. The host may ground P7 if it is not used for Device Activity Signal (DAS).
- 6. P8 and P9 should not be connected by the host.

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5 Supported Command Sets

The Intel SSD DC S3610 Series supports all mandatory ATA (Advanced Technology Attachment) commands defined in the ATA8-ACS specification described in this section.

5.1 ATA General Feature Command Set

The Intel SSD DC S3610 Series supports the ATA General Feature command set (non- PACKET), which consists of:

- EXECUTE DEVICE DIAGNOSTIC
- SET FEATURES
- IDENTIFY DEVICE

Note: See Appendix A, "IDENTIFY DEVICE Command Data" for details on the sector data returned after issuing an IDENTIFY DEVICE command.

The Intel SSD DC S3610 Series also supports the following optional commands:

- READ DMA
- WRITE DMA
- READ SECTOR(S)
- READ VERIFY SECTOR(S)
- READ MULTIPLE
- SEEK
- SET FEATURES
- WRITE SECTOR(S)
- SET MULTIPLE MODE¹
- WRITE MULTIPLE
- FLUSH CACHE
- READ BUFFFER
- WRITE BUFFER
- NOP
- DOWNLOAD MICROCODE
- WRITE UNCORRECTABLE EXT

5.2 Power Management Command Set

The Intel SSD DC S3610 Series supports the Power Management command set, which consists of:

- CHECK POWER MODE
- IDLE
- IDLE IMMEDIATE
- SLEEP
- STANDBY
- STANDBY IMMEDIATE

^{1.} The only multiple supported will be multiple 1



5.3 Security Mode Feature Set

The Intel SSD DC S3610 Series supports the Security Mode command set, which consists of:

- SECURITY SET PASSWORD
- SECURITY UNLOCK
- SECURITY ERASE PREPARE
- SECURITY ERASE UNIT
- SECURITY FREEZE LOCK
- SECURITY DISABLE PASSWORD

5.4 SMART Command Set

The Intel SSD DC S3610 Series supports the SMART command set, which consists of:

- SMART READ DATA
- SMART READ ATTRIBUTE THRESHOLDS
- SMART ENABLE/DISABLE ATTRIBUTE AUTOSAVE
- SMART SAVE ATTRIBUTE VALUES
- SMART EXECUTE OFF-LINE IMMEDIATE
- SMART READ LOG SECTOR
- SMART WRITE LOG SECTOR
- SMART ENABLE OPERATIONS
- SMART DISABLE OPERATIONS
- SMART RETURN STATUS
- SMART ENABLE/DISABLE AUTOMATIC OFFLINE

5.4.1 Attributes

The following table lists the SMART attributes supported by the Intel SSD DC S3610 Series and the corresponding status flags and threshold settings.

Table 19: SMART Attributes

10	A		S					
ID	Attribute	SP	EC	ER	PE	ос	PW	Threshold
05h	Re-allocated Sector Count The raw value of this attribute shows the number of retired blocks since leaving the factory (grown defect count).	1	1	0	0	1	0	0 (none)
09h	Power-On Hours Count The raw value reports power-on time, cumulative over the life of the SSD, integer number in hour time units.	1	1	0	0	1	0	0 (none)
0Ch	Power Cycle Count The raw value of this attribute reports the cumulative number of power cycle events over the life of the device.	1	1	0	0	1	0	0 (none)
AAh	Available Reserved Space (See Attribute E8)	1	1	0	0	1	1	10
ABh	Program Fail Count The raw value of this attribute shows total count of program fails and the normalized value, beginning at 100, shows the percent remaining of allowable program fails.		1	0	0	1	0	0 (none)



	ID Attribute		S					
ID			EC	ER	PE	ос	PW	Threshold
ACh	Erase Fail Count The raw value of this attribute shows total count of erase fails and the normalized value, beginning at 100, shows the percent remaining of allowable erase fails.	1	1	0	0	1	0	0 (none)
AEh	Unexpected Power Loss Also known as "Power-off Retract Count" per magnetic-drive terminology. Reports number of unclean shutdowns, cumulative over the life of the SSD. An "unclean shutdown" is the removal of power without STANDBY IMMEDIATE as the last command (regardless of PLI activity using capacitor power).		1	0	0	1	0	0 (none)
	Power Loss Protection Failure Last test result as microseconds to discharge cap, saturates at max value. Also logs minutes since last test and lifetime number of tests. Bytes 0-1: Last test result as microseconds to dis-							
AFh	charge cap, saturates at max value. Test result expected in range 25 <= result <= 5000000, lower indicates specific error code Bytes 2-3: Minutes since last test, saturates at max value.	1	1	0	0	1	1	10
	Bytes 4-5: Lifetime number of tests, not incremented on power cycle, saturates at max value.							
B7h	SATA Downshift Count The count of the number of times SATA interface selected lower signaling rate due to error.	1	1	0	0	1	0	0 (none)
B8h	End-to-End Error Detection Count Raw value: reports number of LBA tag mismatches in end-to-end data protection path. Normalized value: always 100.		1	0	0	1	1	90
BBh	Uncorrectable Error Count The raw value shows the count of errors that could not be recovered using Error Correction Code (ECC). Normalized value: always 100.	1	1	0	0	1	0	0 (none)
BEh	Temperature - Airflow Temperature (Case) Reports the SSD case temperature. Raw value suggests 100 - case temperature in C degrees.	1	0	0	0	1	0	0 (none)
CØh	Power-Off Retract Count (Unsafe Shutdown Count) The raw value of this attribute reports the cumulative number of unsafe (unclean) shutdown events over the life of the device. An unsafe shutdown occurs whenever the device is powered off without STANDBYIMMEDIATE being the last command.	1	1	0	0	1	0	0 (none)
C2h	Temperature - Device Internal Temperature Reports internal temperature of the SSD. Temperature reading is the value direct from the printed circuit board (PCB) sensor without offset.	1	0	0	0	1	0	0 (none)

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	ID Attribute		S					
ID			EC	ER	PE	ос	PW	Threshold
C5h	Pending Sector Count Number of current unrecoverable read errors that will be re-allocated on next write.	0	1	0	0	1	0	0 (none)
C7h	CRC Error Count The total number of encountered SATA interface cyclic redundancy check (CRC) errors.	1	1	0	0	1	0	0 (none)
E1h	Host Writes The raw value of this attribute reports the total number of sectors written by the host system. The raw value is increased by 1 for every 65,536 sectors (32MB) written by the host.		1	0	0	1	0	0 (none)
E2h	Timed Workload Media Wear Measures the wear seen by the SSD (since reset of the workload timer, attribute E4h), as a percentage of the maximum rated cycles.		1	0	0	1	0	0 (none)
E3h	Timed Workload Host Read/Write Ratio Shows the percentage of I/O operations that are read operations (since reset of the workload timer, attribute E4h).		1	0	0	1	0	0 (none)
E4h	Timed Workload Timer Measures the elapsed time (number of minutes since starting this workload timer).		1	0	0	1	0	0 (none)
E8h	Available Reserved Space This attribute reports the number of reserve blocks remaining. The normalized value begins at 100 (64h), which corresponds to 100 percent availability of the reserved space. The threshold value for this attribute is 10 percent availability.		1	0	0	1	1	10
E9h	Media Wearout Indicator This attribute reports the number of cycles the NAND media has undergone. The normalized value declines linearly from 100 to 1 as the average erase cycle count increases from 0 to the maximum rated cycles. Once the normalized value reaches 1, the number will not decrease, although it is likely that significant additional wear can be put on the device.		1	0	0	1	0	0 (none)
EAh	Thermal Throttle Status Reports Percent Throttle Status and Count of events Byte 0 = Throttling status. Decimal value 0 = No Throttle Applied, 100 = 100% throttling applied. Intermediate percentages are supported. A value larger than 100d is invalid. Bytes 1-4 = Throttling event count. 32 bit counter indicates the number of times thermal throttle has activated. Value is preserved over power cycles. Byte 5 = Reserved		1	0	0	1	0	0 (none)



	Attribute		S					
ID			EC	ER	PE	ос	PW	Threshold
F1h	Total LBAs Written The raw value of this attribute reports the total number of sectors written by the host system. The raw value is increased by 1 for every 65,536 sectors (32MB) written by the host.	1	1	0	0	1	0	0 (none)
F2h	Total LBAs Read The raw value of this attribute reports the total number of sectors read by the host system. The raw value is increased by 1 for every 65,536 sectors (32MB) read by the host.	1	1	0	0	1	0	0 (none)
F3h	Total Bytes Written The raw value of this attribute reports the total number of sectors written to the NAND media. This includes NAND writes triggered by host writes, defrag, background data refresh and wear level relocation writes etc. The raw value is increased by 1 for every 65,536 sectors (32MB) writes to the NAND media. Upon NAND write, new value returned once per minute.	1	1	0	0	1	0	0 (none)

Table 20: SMART Attribute Status Flags

Status Flag	Description	Value = 0	Value = 1
SP	Self-preserving attribute	Not a self-preserving attribute	Self-preserving attribute
EC	Event count attribute	Not an event count attribute	Event count attribute
ER	Error rate attribute	Not an error rate attribute	Error rate attribute
PE	Performance attribute	Not a performance attribute	Performance attribute
ОС	Online collection attribute	Collected only during offline activity	Collected during both offline and online activity
PW	Pre-fail warranty attribute	Advisory	Pre-fail

5.4.2 Timed Workload Endurance Indicators

Timed Workload Media Wear Indicator — ID E2h

This attribute tracks the drive wear seen by the device during the last wear timer loop, as a percentage of the maximum rated cycles. The raw value tracks the percentage up to 2 decimal precision points. This value should be divided by 1024 to get the percentage.

For example: if the raw value is 4455, the percentage is 4455/1024 = 4.35%. The raw value is held at FFFFh until the wear timer (attribute E4h) reaches 60 (minutes) after a SMART EXECUTE OFFLINE IMMEDIATE (B0h/D4h) subcommand 40h to the SSD. The normalized value is always set to 100 and should be ignored.

Timed Workload Host Reads Percentage — ID E3h

This attribute shows the percentage of I/O operations that are read operations during the last workload timer loop. The raw value tracks this percentage and is held at FFFFh until the workload timer (attribute E4h) reaches 60 (minutes). The normalized value is always set to 100 and should be ignored.



Workload Timer — ID E4h

This attribute is used to measure the time elapsed during the current workload. The attribute is reset when a SMART EXECUTE OFFLINE IMMEDIATE (D4h) subcommand 40h is issued to the drive. The raw value tracks the time in minutes and has a maximum value of $2^{32} = 4,294,967,296$ minutes (8,171 years). The normalized value is always set to 100 and should be ignored.

User Notes

- Sending a SMART EXECUTE OFFLINE IMMEDIATE (B0h/D4h) subcommand 40h to the SSD resets and starts all three attributes (Media Wear Indicator, Attribute E2h, Host Reads Percentage, Attribute E3h, and the Workload timer, Attribute E4h) to FFFFh.
- The Attribute raw values are held at FFFFh until the Workload timer (Attribute E4h) reaches a total of 60 (minutes) of power on time. After 60 minutes, the Timed Workload data is made available.
- After the Workload timer (E4h) reaches 60 (minutes), the Timed Workload data is saved every minute so only 59 seconds of data is lost if power is removed without receiving ATA STANDBY IMMEDIATE. Accumulated data is not reset due to power loss.
- Upon power up, the attributes hold a snapshot of their last saved values for 59 seconds and live data is available after 60 seconds, once the initial one hour interval is completed.

Example Use Cases

The Timed Workload Endurance attributes described in this section are intended to be used to measure the amount of media wear that the drive is subjected to during a timed workload.

Ideally, the system that the drive is being used in should be capable of issuing SMART commands. Otherwise, provisions have been provided to allow the media wear attributes to be persistent so the drive can be moved to a SMART capable system to read out the drive wear attribute values.

Use Case 1 - With a System Capable of SMART Commands

- On a SMART capable system, issue the SMART EXECUTE OFF-LINE IMMEDIATE (D4h) subcommand 40h to reset the drive wear attributes.
- 2. Run the workload to be evaluated for at least 60 minutes. Otherwise the drive wear attributes will not be available.
- 3. Read out the drive wear attributes with the SMART READ DATA (D0h) command.

Use Case 2 - With a System Not Capable of SMART Commands

- On a SMART capable system, issue the SMART EXECUTE OFF-LINE IMMEDIATE (D4h) subcommand 40h to reset the drive wear attributes.
- Move the drive to the system where the workload will be measured (and not capable of SMART commands).
- 3. Run the workload to be evaluated for at least 60 minutes. Otherwise the drive wear attributes will not be available.
- 4. Do a clean system power down by issuing the ATA STANDBY IMMEDIATE command prior to shutting down the system. This will store all the drive wear SMART attributes to persistent memory within the drive.
- 5. Move the drive to a SMART capable system.
- 6. Read out the drive wear attributes with the SMART READ DATA (D0h) command within 59 seconds after power-up.

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Example Calculation of Drive Wear

The following is an example of how the drive wear attributes can be used to evaluate the impact of a given workload. The Host Writes SMART attribute (E1h) can also be used to calculate the amount of data written by the host during the workload by reading this attribute before and after running the workload. This example assumes that the steps shown in "Example Use Cases" on page 18 were followed to obtain the following attribute values:

- Timed Workload Media Wear (E2h) has a raw value of 16. Therefore, the percentage wear = 16/1024 = 0.016%.
- Timed Workload Host Read/Write Ratio (E3h) has a normalized value of 80, indicating that 80% of operations were reads.
- Workload Timer (E4h) has a raw value of 500. Therefore the workload ran for 500 minutes.
- Host Writes Count (E1h) had a raw value of 100,000 prior to running the workload and a value of 130,000 at the end of the workload. Therefore, the number of sectors written by the host during the workload was 30,000 * 65,535 = 1,966,050,000 sectors or 1,966,050,000 * 512/1,000,000,000 = 1,007 GB.

The following conclusions can be made for this example case:

The workload took 500 minutes to complete with 80% reads and 20% writes. A total of 1,007 GB of data was written to the device, which increased the media wear in the drive by 0.016%. At this point in time, this workload is causing a wear rate of 0.016% for every 500 minutes, or 0.00192%/hour.

5.4.3 SMART Logs

The Intel SSD DC S3610 Series implements the following Log Addresses: 00h, 02h, 03h, 06h, and 07h.

The DC S3610 Series implements host vendor specific logs (addresses 80h-9Fh) as read and write scratchpads, where the default value is zero (0). Intel SSD DC S3610 does not write any specific values to these logs unless directed by the host through the appropriate commands.

The DC S3610 Series also implements a device vendor specific log at address A9h as a read-only log area with a default value of zero (0). Besides that, the DC S3610 Series also implements log address B8h (if the drive is in disable logical mode, log address B8h will have the word error code for *BAD_CTX). Finally the DC S3610 Series also implements log at addresses B9h and BAh (both of them are Intel® error logs, and read only for customers).

5.5 Device Statistics

In addition to the SMART attribute structure, statistics pertaining to the operation and health of the Intel SSD DC S3610 Series can be reported to the host on request through the Device Statistics log as defined in the ATA specification.

The Device Statistics log is a read-only GPL/SMART log located at read log address 0x04 and is accessible using READ LOG EXT, READ LOG DMA EXT or SMART READ LOG commands.

Table 20 lists the Device Statistics supported by the Intel SSD DC S3610 Series.



Table 21: Serial ATA Power Pin Definitions—2.5-inch Form Factors

Page	Offset	Description	Equivalent SMART attribute (if applicable)
0x00		List of Supported Pages	
	0x08	Power Cycle Count	0Ch
	0x10	Power-On Hours	09h
	0x18	Logical Sectors Written	E1h
0x01 – General Statistics	0x20	Num Write Commands – incremented by one for every host write	
	0x28	Logical Sectors Read	F2h
	0x30	Num Read Commands – incremented by one for every host read	
	0x08	Num Reported Uncorrectable Errors	BBh
0x04 – General Error Statistics	0x10	Num Resets Between Command Acceptance and Completion	
	0x00	Device Statistics Information Header	
	0x08	Current Temperature	
	0x10	Average Short Term Temperature	
	0x18	Average Long Term Temperature	
	0x20	Highest Temperature	
	0x28	Lowest Temperature	
0.05 7	0x30	Highest Average Short Term Temperature	
0x05 – Temperature Statistics	0x38	Lowest Average Short Term Temperature	
	0x40	Highest Average Long Term Temperature	
	0x48	Lowest Average Long Term Temperature	
	0x50	Time in Over-Temperature	
	0x58	Specified Maximum Operating Temperature	
	0x60	Time in Under-Temperature	
	0x68	Specified Minimum Operating Temperature	
	0x08	Number of Hardware Resets	
0x06 – Transport Statistics	0x10	Number of ASR Events	
	0x18	Number of Interface CRC Errors	
0x07 – Solid State Device Statistics	0x08	Percentage Used Endurance Indicator	E9h Note: This device statistic counts from 1 to 150



5.6 SMART Command Transport (SCT)

With SMART Command Transport (SCT), a host can send commands and data to an SSD and receive status and data from an SSD using standard write/read commands to manipulate two SMART Logs:

- Log Address E0h ("SCT Command/Status") used to send commands and retrieve status
- Log Address E1h ("SCT Data Transfer") used to transport data
 Intel SSD DC S3610 supports the following standard SCT actions:
- Write Same DC S3610 Series implements this action code as described in the ATA specification.
- Error Recovery Control DC S3610 Series accepts this action code, and will store and return error-recovery time limit values.
- Feature Control DC S3610 Series supports feature code 0001h (write cache) feature code 0002h (write cache reordering), and feature code 0003h (time interval for temperature logging). It also supports D000h (Power Safe Write Cache capacitor test interval), D001h (read/write power governor mode), D002h (read thermal governor mode), D003h (read power governor burst power), and D004h (read power governor average power).
- Data table command DC S3610 Series supports data table command as specified in ATA8-ACS2. This will read out temperature logging information in table ID 0002h.
- Read Status Support DC S3610 supports read status log
- SCT command 0xD801with State=0, Option=1, ID Word 106 can be used to change from 0x6003 to 0x4000 (4KB physical sector size to 512B physical sector size support change).

5.7 Data Set Management Command Set

Intel SSD DC S3610 Series supports the Data Set Management command set Trim attribute, which consists of:

• DATA SET MANAGEMENT

5.8 Host Protected Area Command Set

Intel SSD DC S3610 Series supports the Host Protected Area command set, which consists of:

- READ NATIVE MAX ADDRESS
- SET MAX ADDRESS
- READ NATIVE MAX ADDRESS EXT
- SET MAX ADDRESS EXT

Intel SSD DC S3610 Series also supports the following optional commands:

- SET MAX SET PASSWORD
- SET MAX LOCK
- SET MAX FREEZE LOCK
- SET MAX UNLOCK



5.9 48-Bit Address Command Set

Intel SSD DC S3610 Series supports the 48-bit Address command set, which consists of:

- FLUSH CACHE EXT
- READ DMA EXT
- READ NATIVE MAX ADDRESS EXT
- READ SECTOR(S) EXT
- READ VERIFY SECTOR(S) EXT
- SET MAX ADDRESS EXT
- WRITE DMA EXT
- WRITE MULTIPLE EXT
- WRITE SECTOR(S) EXT
- WRITE MULTIPLE FUA EXT
- WRITE DMA FUA EXT

5.10 General Purpose Log Command Set

Intel SSD DC S3610 Series supports the General Purpose Log command set, which consists of:

- READ LOG EXT
- WRITE LOG EXT

5.11 Native Command Queuing

Intel SSD DC S3610 Series supports the Native Command Queuing (NCQ) command set, which includes:

- READ FPDMA QUEUED
- WRITE FPDMA QUEUED

Note: With a maximum Queue Depth set to 32.

5.12 Software Settings Preservation

Intel SSD DC S3610 Series supports the SET FEATURES parameter to enable/disable the preservation of software settings.



6 Certifications and Declarations

The following table describes the Device Certifications supported by the Intel SSD DC S3610 Series.

Table 22: Device Certifications and Declarations

Certification	Description
CE Compliant	Low Voltage DIRECTIVE 2006/95/EC OF THE EUROPEAN PARLIAMENT AND OF THE COUNCIL of 12 December 2006, and EMC Directive 2004/108/EC OF THE EUROPEAN PARLIAMENT AND OF THE COUNCIL of 15 December 2004.
UL Recognized CSA C22.2 No. 60950-1-07, 2nd Edition, 2007-03 (Information Technology Equipmer Part 1: General Requirements)	
C-Tick Compliant Compliance with the Australia/New Zealand Standard AS/NZS3548 and Electro Compatibility (EMC) Framework requirements of the Australian Communication	
BSMI Compliant	Compliance to the Taiwan EMC standard CNS 13438: Information technology equipment - Radio disturbance Characteristics - limits and methods of measurement, as amended on June 1, 2006, is harmonized with CISPR 22: 2005.04.
ксс	Compliance with paragraph 1 of Article 11 of the Electromagnetic Compatibility Control Regulation and meets the Electromagnetic Compatibility (EMC) Framework requirements of the Radio Research Laboratory (RRL) Ministry of Information and Communication Republic of Korea.
VCCI	Voluntary Control Council for Interface to cope with disturbance problems caused by personal computers or facsimile.
RoHS Compliant	Restriction of Hazardous Substance Directive
WEEE	Directive on Waste Electrical and Electronic Equipment



7 References

The following table identifies the standards information referenced in this document.

Table 23: Standards References

Date	Title	Location
July 2012	Solid-State Drive (SSD) Requirements and Endurance Test Method (JESD219)	http://www.jedec.org/standards- documents/results/jesd219
Sept 2010	Solid-State Drive (SSD) Requirements and Endurance Test Method (JESD218)	http://www.jedec.org/standards-documents/docs/jesd218/
Dec 2008	vccı	http://www.vcci.jp/vcci_e/
June 2009	RoHS	http://qdms.intel.com/ Click Search MDDS Database and search for material description datasheet
August 2009	ACS-2-ATA/ATAPI Command Set 2 Specification	http://www.t13.org/
June 2009	Serial ATA Revision 3.0	http://www.sata-io.org/
May 2006	SFF-8223, 2.5-inch Drive w/Serial Attachment Connector	http://www.sffcommittee.org/
May 2005	SFF-8201, 2.5-inch drive form factor	http://www.sffcommittee.org/
1995 1996 1995 1995 1997 1994	International Electrotechnical Commission EN 61000 4-2 (Electrostatic discharge immunity test) 4-3 (Radiated, radio-frequency, electromagnetic field immunity test) 4-4 (Electrical fast transient/burst immunity test) 4-5 (Surge immunity test) 4-6 (Immunity to conducted disturbances, induced by radio-frequency fields) 4-11 (Voltage Variations, voltage dips, short interruptions and voltage variations immunity tests)	http://www.iec.ch/
1995	ENV 50204 (Radiated electromagnetic field from digital radio telephones)	http://www.dbicorporation.com/ radimmun.htm/



Appendix A: IDENTIFY DEVICE Command Data

Table 24: Returned Sector Data

Table 24:	Returned Sec	tor Data	
Word	F = Fixed V = Variable X = Both	Default Value	Description
0	Х	0040h	General configuration bit-significant information
1	Х	3FFFh	Obsolete - Number of logical cylinders (16,383)
2	V	C837h	Specific configuration
3	Х	0010h	Obsolete - Number of logical heads (16)
4-5	Х	Oh	Retired
6	Х	003Fh	Obsolete - Number of logical sectors per logical track (63)
7-8	V	Oh	Reserved for assignment by the CompactFlash* Association (CFA)
9	Х	Oh	Retired
10-19	F	varies	Serial number (20 ASCII characters)
20-21	Х	Oh	Retired
22	Х	Oh	Obsolete
23-26	F	varies	Firmware revision (8 ASCII characters)
27-46	F	varies	Model number (Intel [®] Solid-State Drive)
47	F	8001h	7:0—Maximum number of sectors transferred per interrupt on multiple commands
48	F	4000h	Trusted Computing Feature Set
49	F	2F00h	Capabilities
50	F	4000h	Capabilities
51-52	Х	0h	Obsolete
53	F	0007h	Words 88 and 70:64 valid
54	Х	3FFFh	Obsolete - Number of logical cylinders (16,383)
55	Х	0010h	Obsolete - Number of logical heads (16)
56	Х	003Fh	Obsolete - Number of logical sectors per logical track (63)
57-58	X	FC1000FBh	Obsolete
59	F	BF01	Number of sectors transferred per interrupt on multiple commands
60-61	V	100GB: BA52230h 200GB: 0FFFFFFFh 400GB: 0FFFFFFFh 480GB: 0FFFFFFFh 800GB: 0FFFFFFFh 1200GB: 0FFFFFFFh 1600GB: 0FFFFFFFh	Total number of user-addressable sector for 28-bit commands
62	X	0h	Obsolete
63	Χ	0007h	Multi-word DMA modes supported/selected
64	F	0003h	PIO modes supported
65	F	0078h	Minimum multiword DMA transfer cycle time per word
66	F	0078h	Manufacturer's recommended multiword DMA transfer cycle time
67	F	0078h	Minimum PIO transfer cycle time without flow control
68	F	0078h	Minimum PIO transfer cycle time with IORDY flow control
69	F	4030h	Additional Supported
70	F	0000h	Reserved
71-74	F	0h	Reserved for IDENTIFY PACKET DEVICE command
75	F	001Fh	Queue depth



X = Both	Word	F = Fixed V = Variable	Default Value	Description
77 F 0006h Reserved for future Serial ATA definition 78 F 0040h Serial ATA features supported 79 V 0040h Serial ATA features supported 80 F 03FCh Major version number 81 F 0110h Minor version number 81 F 7468h Command set supported 82 F 7468h Command set Supported 83 F 7501h Command set Supported 84 F 6163h Command set/feature enabled 85 X 7469h Command set/feature enabled 86 X B401h Command set/feature enabled 87 X 6163h Command set/feature enabled 88 X 407Fh Ultra DMA Modes 89 F 0002h Time required for enhanced security erase completion 90 F 0002h Time required for enhanced security erase completion 91 V Oh Compand set/feature default <tr< td=""><td></td><td>Y = ROLU</td><td></td><td></td></tr<>		Y = ROLU		
78 F 0040h Serial ATA features supported 79 V 0040h Serial ATA features enabled 80 F 03FCh Major version number 81 F 0110h Minor version number 82 F 746Bh Command set supported 83 F 7501h Command set/feature supported extension 84 F 6163h Command set/feature enabled 86 X B401h Command set/feature enabled 87 X 6163h Command set/feature enabled 88 X 407Fh Ultra DMA Modes 89 F 0002h Time required for security erase unit completion 90 F 0002h Time required for security erase completion 91 V Oh Current advanced power management value 92 V OFFFEH Master Password Revision Code 93 X Oh Hardware reset result the contents of bits (12:0) of this word shall change only during the execution of a hardware reset 95	-			·
79 V 0040h Serial ATA features enabled 80 F 03FCh Major version number 81 F 0110h Minor version number 82 F 746Bh Command set supported 83 F 7501h Command set/feature enabled 84 F 6163h Command set/feature enabled 85 X 7469h Command set/feature enabled 86 X B401h Command set/feature enabled 87 X 6163h Command set/feature enabled 88 X 407Fh Ultra DMA Modes 89 F 0002h Time required for security erase unit completion 90 F 0002h Time required for security erase unit completion 91 V 0h Current advanced power management value 92 V 0FFFEh Master Password Revision Code 93 X 0h Hardware reset result the contents of bits (12:0) of this word shall change only during the execution of a hardware reset 94 <td< td=""><td>77</td><td></td><td>0006h</td><td>Reserved for future Serial ATA definition</td></td<>	77		0006h	Reserved for future Serial ATA definition
80 F 03FCh Major version number 81 F 0110h Minor version number 82 F 7468h Command sets supported 83 F 7501h Command set Supported 84 F 6163h Command set/feature supported extension 85 X 7469h Command set/feature enabled 86 X B401h Command set/feature default 88 X 407Fh Ultra DMA Modes 89 F 0002h Time required for security erase unit completion 90 F 0002h Time required for security erase completion 91 V Oh Current advanced power management value 92 V OFFEE Master Password Revision Code 93 X Oh Hardware reset result: the contents of bits (12:0) of this word shall change only during the execution of a hardware reset 94 V Oh Vendor's recommended and actual acoustic management value 95 F Oh Streaming transfer time - DMA			0040h	Serial ATA features supported
81 F 0110h Minor version number 82 F 7468h Command set supported 83 F 7501h Command set supported 84 F 6163h Command set/feature supported extension 85 X 7469h Command set/feature enabled 86 X B401h Command set/feature enabled 87 X 6163h Command set/feature enabled 88 X 407Fh Ultra DMA Modes 89 F 0002h Time required for security erase unit completion 90 F 0002h Time required for security erase completion 91 V Oh Current advanced power management value 92 V OFFFEH Master Password Revision Code 93 X Oh Hardware reset result the contents of bits (12:0) of this word shall change only during the execution of a hardware reset 94 V Oh Streaming transfer time - DMA 97 V Oh Streaming transfer time - DMA 98-99				
82	+			<u> </u>
83	+			
84	-			
85	83	F	7501h	
86 X B401h Command set/feature enabled 87 X 6163h Command set/feature default 88 X 407Fh Ultra DMA Modes 89 F 0002h Time required for security erase unit completion 90 F 0002h Time required for enhanced security erase completion 91 V 0h Current advanced power management value 92 V 0FFFEh Master Password Revision Code Hardware reset result: the contents of bits (12:0) of this word shall change only during the execution of a hardware reset 400 93 X 0h Vendor's recommended and actual acoustic management value 95 F 0h Stream inimimum request size 96 V 0h Streaming transfer time - DMA 97 V 0h Streaming performance granularity 100GB: BA52230h 200GB: 1749F1B0h 400GB: 2230h 200GB: 125930B0h 1600GB: 25230b0h 1600GB: 2526CEB0h 1200GB: 2526CEB0h 160GB: 2526CEBDB: 2526CEB0h 16	84		6163h	
87 X 6163h Command set/feature default 88 X 407Fh Ultra DMA Modes 89 F 0002h Time required for security erase unit completion 90 F 0002h Time required for enhanced security erase completion 91 V 0h Current advanced power management value 92 V 0FFFEh Master Password Revision Code 93 X 0h Hardware reset result: the contents of bits (12:0) of this word shall change only during the execution of a hardware reset 94 V 0h Vendor's recommended and actual acoustic management value 95 F 0h Stream minimum request size 96 V 0h Streaming transfer time - DMA 97 V 0h Streaming performance granularity 100GB: BA52230h 200GB: 1749F1B0h 400GB: 2230h 200GB: 1749F1B0h 200GB: 25026CEB0h 1200GB: 25026CEB0h 1200GB: 25026CEB0h 1200GB: 25026CEB0h 1200GB: 25026CEB0h 1200GB: 26026BBA0CB0h 1600GB: 26026BBA0CB0h 1600GBBA0CB0BBA0CB0BBA0CB0BBA0CB0BBA0CB0BBA0CB0BBA0CB0BBA0CB0BBA0CB0BBA0CB0BBA0CB0BBA0CB0BBA0CB0	85	X	7469h	
88 X 407Fh Ultra DMA Modes 89 F 0002h Time required for security erase unit completion 90 F 0002h Time required for security erase completion 91 V 0h Current advanced power management value 92 V 0FFFEh Master Password Revision Code 93 X 0h Hardware reset result: the contents of bits (12:0) of this word shall change only during the execution of a hardware reset 94 V 0h Vendor's recommended and actual acoustic management value 95 F 0h Stream minimum request size 96 V 0h Streaming transfer time - DMA 97 V 0h Streaming access latency - DMA and PIO 98-99 F 0h Streaming performance granularity 1006B: BA52230h 2006B: 37E43680h 800GB: 37E43680h 800GB: 5026CEB0h 1200GB: 8BBA0CB0h 1600GB: BA4D4B0h Maximum user LBA for 48-bit address feature set 100 V 0h Streaming transfer time - PIO 105 V 0h Maximum number of 512-byte blocks of LBA Range Entries per DATA SET MANAGEMENT com	86		B401h	
B9	87	Χ		Command set/feature default
90 F 0002h Time required for enhanced security erase completion 91 V 0h Current advanced power management value 92 V 0FFFEh Master Password Revision Code 93 X 0h Hardware reset result: the contents of bits (12:0) of this word shall change only during the execution of a hardware reset 94 V 0h Vendor's recommended and actual acoustic management value 95 F 0h Stream minimum request size 96 V 0h Streaming transfer time - DMA 97 V 0h Streaming access latency - DMA and PIO 98-99 F 0h Streaming performance granularity 100GB: 8A52230h 200GB: 1749F180h 400GB: 2E939080h 480GB: 37E43680h 800GB: 5126CEB0h 1200GB: 8BBAOCBOh 1600GB: 8BBAOCBOh 1600GB: 8BBAOCBOh 1600GB: 8BBAOCBOh 1600GB: BAAD4AB0h 104 V 0h Streaming transfer time - PIO 105 V 0006h Maximum number of 512-byte blocks of LBA Range Entries per DATA SET MANAGEMENT command 106 F 6003h Default Physical sector size / Logical sector size. Can be changed to 0004h to reflect 512Bytes/Sector 107 F 0h Inter-seek delay for ISO-7779 acoustic testing in microseconds 106-111 F varies Unique ID 112-115 F 0h Reserved for worldwide name extension to 128 bits	88	X	407Fh	
91	89	F		Time required for security erase unit completion
92	90		0002h	
93 X	91	V		
93	92	V	0FFFEh	
95	93	X	0h	
96	94	V	0h	Vendor's recommended and actual acoustic management value
97 V Oh Streaming access latency - DMA and PIO 98-99 F Oh Streaming performance granularity 100-103 Investigation of the properties of the p	95	F	0h	Stream minimum request size
98-99 F	96	V	0h	Streaming transfer time - DMA
100GB: BA52230h	97	V	0h	Streaming access latency - DMA and PIO
BA52230h	98-99	F	0h	Streaming performance granularity
105 V 0006h Maximum number of 512-byte blocks of LBA Range Entries per DATA SET MANAGEMENT command 106 F 6003h Default Physical sector size / logical sector size. Can be changed to 0004h to reflect 512Bytes/Sector 107 F 0h Inter-seek delay for ISO-7779 acoustic testing in microseconds 108-111 F varies Unique ID 112-115 F 0h Reserved for worldwide name extension to 128 bits 116 V 0h Reserved for technical report	100-103	V	BA52230h 200GB: 1749F1B0h 400GB: 2E9390B0h 480GB: 37E436B0h 800GB: 5D26CEB0h 1200GB: 8BBA0CB0h 1600GB:	Maximum user LBA for 48-bit address feature set
105 V 0006h Maximum number of 512-byte blocks of LBA Range Entries per DATA SET MANAGEMENT command 106 F 6003h Default Physical sector size / logical sector size. Can be changed to 0004h to reflect 512Bytes/Sector 107 F 0h Inter-seek delay for ISO-7779 acoustic testing in microseconds 108-111 F varies Unique ID 112-115 F 0h Reserved for worldwide name extension to 128 bits 116 V 0h Reserved for technical report	104	V		Streaming transfer time - PIO
Can be changed to 0004h to reflect 512Bytes/Sector 107 F Oh Inter-seek delay for ISO-7779 acoustic testing in microseconds 108-111 F varies Unique ID 112-115 F Oh Reserved for worldwide name extension to 128 bits 116 V Oh Reserved for technical report	105	V	0006h	Maximum number of 512-byte blocks of LBA Range Entries per
108-111 F varies Unique ID 112-115 F 0h Reserved for worldwide name extension to 128 bits 116 V 0h Reserved for technical report	106	F	6003h	
112-115 F Oh Reserved for worldwide name extension to 128 bits 116 V Oh Reserved for technical report	107	F	0h	Inter-seek delay for ISO-7779 acoustic testing in microseconds
116 V 0h Reserved for technical report	108-111	F	varies	Unique ID
	112-115	F	0h	Reserved for worldwide name extension to 128 bits
	116	V	0h	Reserved for technical report
	117-118	F	0h	



Word	F = Fixed V = Variable X = Both	Default Value	Description
119	F	405Ch	Supported settings
120	Х	401Ch	Command set/feature enabled/supported
121-126	F	0h	Reserved
127	Х	0h	Removable Media Status Notification feature set support
128	Х	0021h	Security status
129	V	001Ch	Vendor-specific
130-139	Х	0h	Vendor-specific
140-149	X	0h	Disable Logical Error Field
150-159	Х	0h	Vendor-specific
160	Х	0h	CompactFlash Association (CFA) power mode 1
161-167	Х	0h	Reserved for assignment by the CFA
168	Х	0003h	Reserved for assignment by the CFA
169	Х	0001h	Data set management Trim attribute support
170-175	F	0h	Reserved for assignment by the CFA
176-205	V	Varies	Current media serial number
206	Х	003Dh	SCT Command Transport
207-208	F	0000h	Reserved
209	X	4000h	Alignment of logical blocks within a physical block
210-211	V	0000h	Write-Read-Verify Sector Count Mode 3 (DWord)
212-213	F	0000h	Write-Read-Verify Sector Count Mode 2 (DWord)
214	X	0000h	NV Cache Capabilities
215-216	V	0000h	NV Cache Size in Logical Blocks (DWord)
217	F	0001h	Nominal media rotation rate
218	V	0000h	Reserved
219	F	0000h	NV Cache Options
220	V	0000h	Write-Read-Verify feature set
221	X	0000h	Reserved
222	F	101Fh	Transport major version number
223	F	0000h	Transport minor version number
224-229	F	0000h	Reserved
230-233	X	0000h	Extended Number of User Addressable Sectors (QWord)
234	F	0001h	Minimum number of 512-byte data blocks per DOWNLOAD MICROCODE command for mode 03h
235	F	FFFFh	Maximum number of 512-byte data blocks per DOWNLOAD MICROCODE command for mode 03h
236-254	Х	0000h	Reserved
255	V	Varies	Integrity word

Notes:

 $\mathbf{X} = \mathbf{F} \mathbf{or} \mathbf{V}$. The content of the word may be fixed or variable.

F = Fixed. The content of the word is fixed and does not change. For removable media devices, these values may change when media is removed or changed.

V = Variable. The state of at least one bit in a word is variable and may change depending on the state of the device or the commands executed by the device.